

Revision History

Rev.	Date	Author	Description
0.1	02/02/01	Igor Mohor	First Draft
0.2	05/04/01		

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IO Ports

2.1 JTA

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Operation

This section describes the operation of the JTAG interface, TAP controller, supported instructions,

4.2.1 EXTEST (IR=0000)

The EXTEST instruction connects the



- € UpdatedDR state: The data shifted in via TDI is ignored (ID is a read-only register).

4.2.4 DEBUG (IR=1000)

The



The last three scan chains are

